

**AMENDMENT TO THE DRAWING**

Figs. 2, 4 and 6 have been amended. The attached sheets are proposed to replace the original sheets including Figs. 2, 4 and 6.

**REMARKS/ARGUMENTS**

This is responsive to the Office Action dated January 3, 2005.

The Examiner objected to Figure 6 on the ground that no bias circuit is shown for the bases of the mirror transistors. The objection is respectfully traversed. A patent application does not have to disclose that which is well known. The Examiner's three reference patents show a similar drafting style. A skilled person could construct a working circuit based on Figure 6 without undue experimentation.

Claims 1-3 were rejected as being non-enabled under 35 U.S.C. §112, first paragraph. Reconsideration is requested. The claims are being amended to state that the comparator has respective inputs receiving "voltages representative of  $V_{be}$  and  $\Delta V_{be}$ " as seen at IN- and IN+ in both Figs. 4 and 6. Note that  $V_{be}$  is related to the emitter current of Q1 and therefore to the voltage at the node between R1 and R2. Incidentally, enclosed are marked-up formal drawing sheets showing proposed corrections to Figs. 2, 4 and 6. Approval is requested.

Claims 1-3 were rejected as being indefinite under 35 U.S.C. §112, second paragraph. The Examiner objected to the words "substantially proportional to" in claim 1. These words are being replaced by - - representative of - -, which meets the Examiner's requirements. Similarly, with the drawings being revised to use consistent references, the claims are readable on the drawings as discussed in the specification.

Claim 2 was not rejected over prior art. Allowance is requested.

Claims 1 and 3 were rejected as being anticipated by Tasdighi. Claims 1 and 3 are being amended to recite the presence of both "a first bipolar transistor" and "two additional bipolar transistors," for a total of three. Tasdighi's Fig. 1B cited by the Examiner has only two bipolar transistors and does not operate in the manner recited in the amended claims. Allowance is therefore requested.

**FACSIMILE CERTIFICATE**

I hereby certify that this correspondence is being sent via facsimile (703) 872-9306 to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on June 3, 2005:

James A. Finder

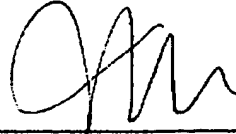
Name of applicant, assignee or  
Registered Representative

Signature

June 3, 2005

Date of Signature

Respectfully submitted,



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